IN THE SPECIFICATION

Kindly amend the specification as follows:

[0007] One example of a typical circuit that can be used for detecting charge in the particular n+ type diffusion node is shown as a schematic in FIG. 1. The circuit consists of reset transistor 117 that connects charge detection node 115 to reference voltage terminal 119 when a suitable reset level is applied to gate 118. Photo-generated charge accumulating on node 115 causes a voltage charge that is buffered by transistor 116 with its drain connected to Vdd bias terminal 120. The output signal then appears on node 121 and can be further processed either as a voltage or as a current when supplied to the rest of the sensor circuitry. Circuit ground 122 is identical to p+ type doped substrate 101. For ~~simplicity, only one schematic circuit is shown, although there are typically three for a single pixel sensing three colors.~~ a single pixel that senses three colors, each color has a circuit including reset transistor 117 and amplifier transistor 116, connected as shown in FIG. 1. It would be apparent to those skilled in the art that other, more complex circuits can be connected to pixel 100.

[0025] The remainder of pixel 200 operates in a manner similar to pixel 100. Oxide dielectric layer 210, channel stops 209, metal contacts 211, 212, and 213, together with wiring 214 serve the same purpose in pixel 200 as in pixel 100. Also, pixel 200 is the same with reset and buffer transistors 217 and 216 respectively, reset gate terminal 218, reference voltage terminal 219, Vdd bias

terminal 220, and output terminal 221 shown connected to each of plugs 206, 207, and 208. The circuit ground is terminal 222.

[0033] The remainder of the structure is similar to the previous example. P+ type doped channel stop regions 409 separate n+ type charge detection node junctions 406, 407, and 408 from each other. Detection node junctions 406, 407, and 408 are connected to metallization regions 411, 412, and 413 through contact holes opened in oxide dielectric layer 410. Wires 414 are used for interconnecting detection node junctions 406, 407 and 408 with the rest of the circuit components of pixel 400, such as the reset transistors 417 and the buffer transistors 416~~, for detection node junction 407~~ shown in FIG. 4 connected to each of plugs 406, 407, and 408.